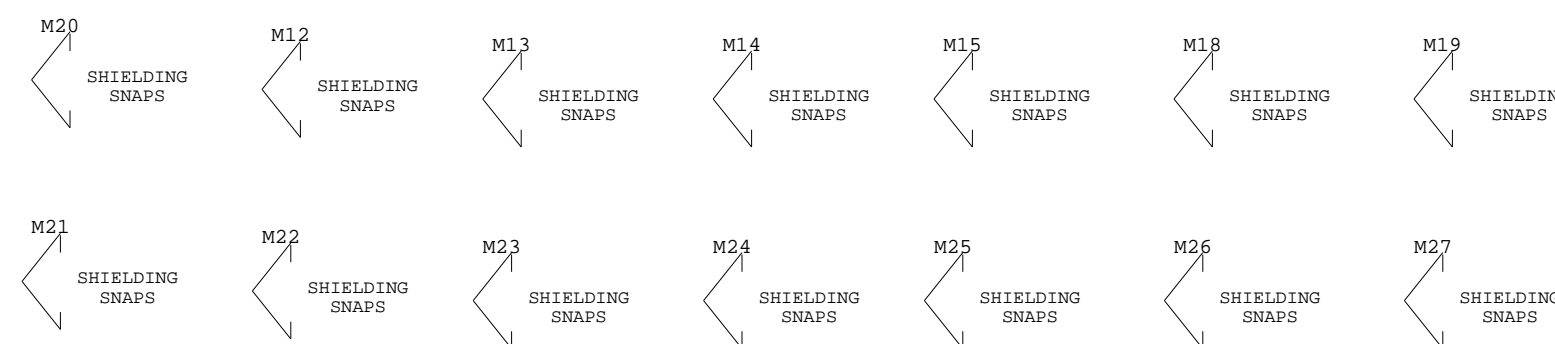
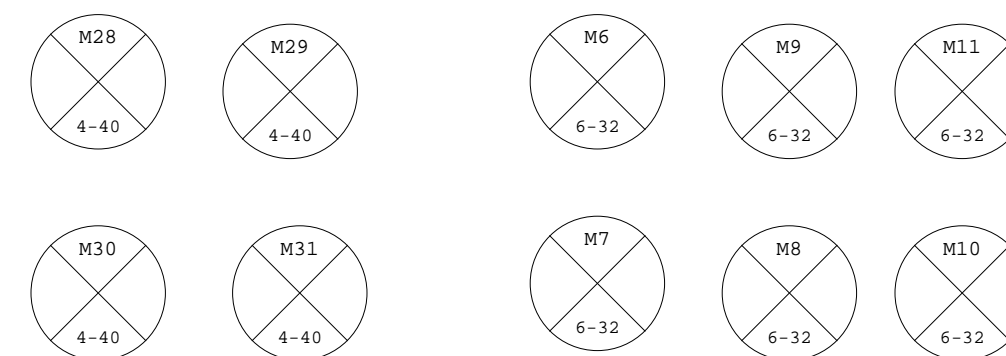
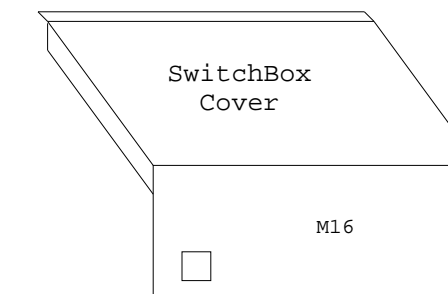
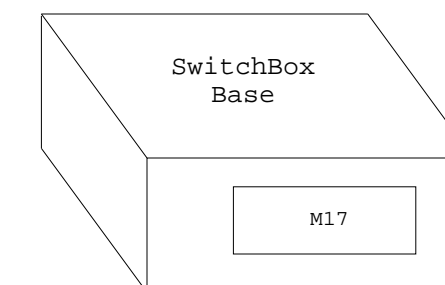
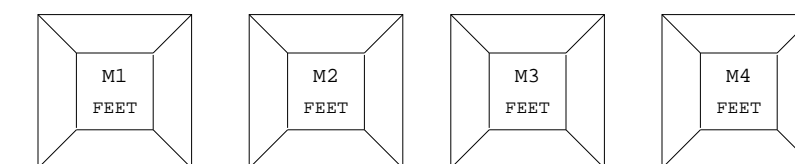
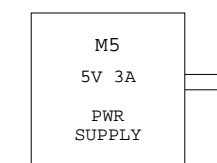


Power Supply
Page 6

External Parts

Power Supply

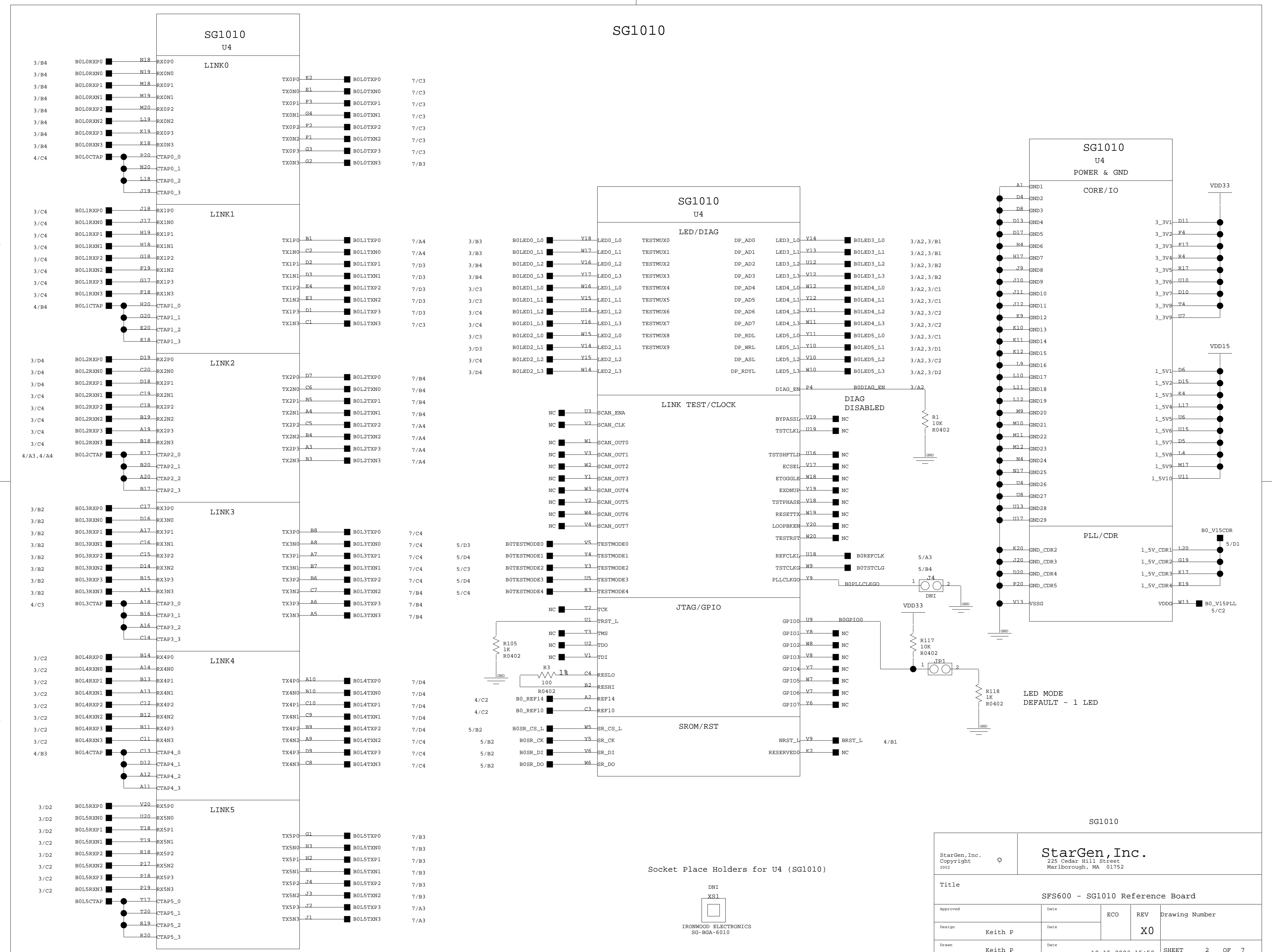
+5V 3A



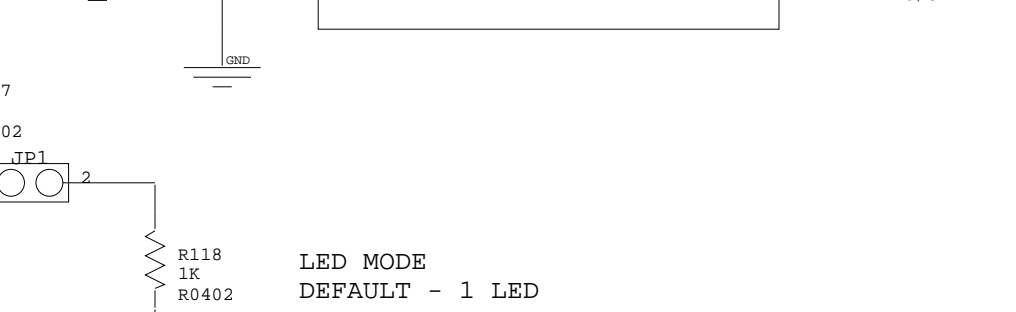
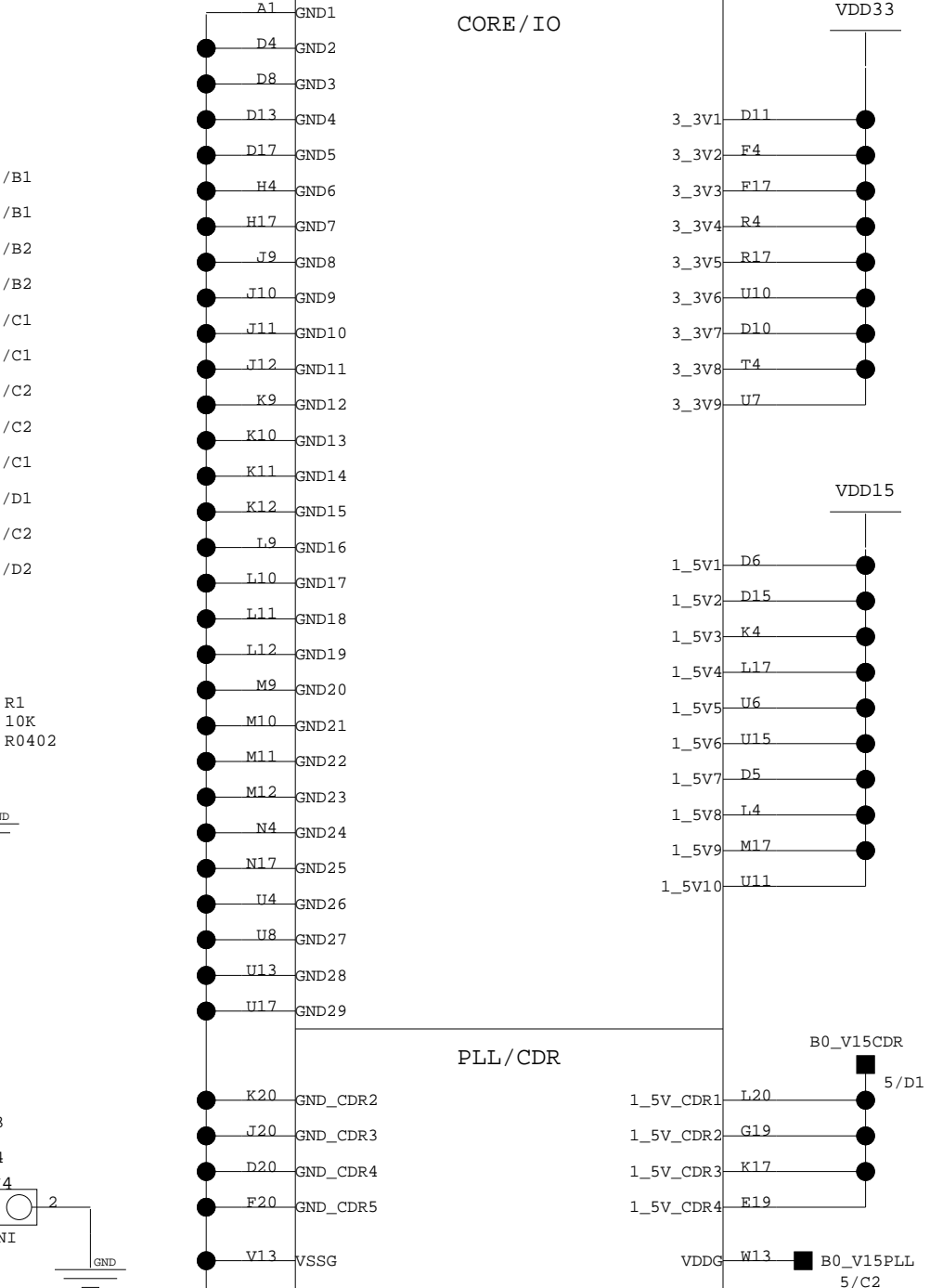
Page	Contents
1	Block Diagram, TOC, Mech Parts
2	SG1010 SWITCH
3	RJ45 CONNECTOR, LINK LEDS
4	CTAP, REF10 AND REF14, PWR-ON RST
5	TSTMODE, SROMS, ANLG PWR, CLKS
6	1.5V REG & 3.3V REG, DECOUPLING CAPS
7	COMMON MODE CHOKES

BLOCK DIAGRAM, TABLE OF CONTENTS

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Approved	Date	ECO	REV	Drawing Number
Design Keith P	Date		X0	
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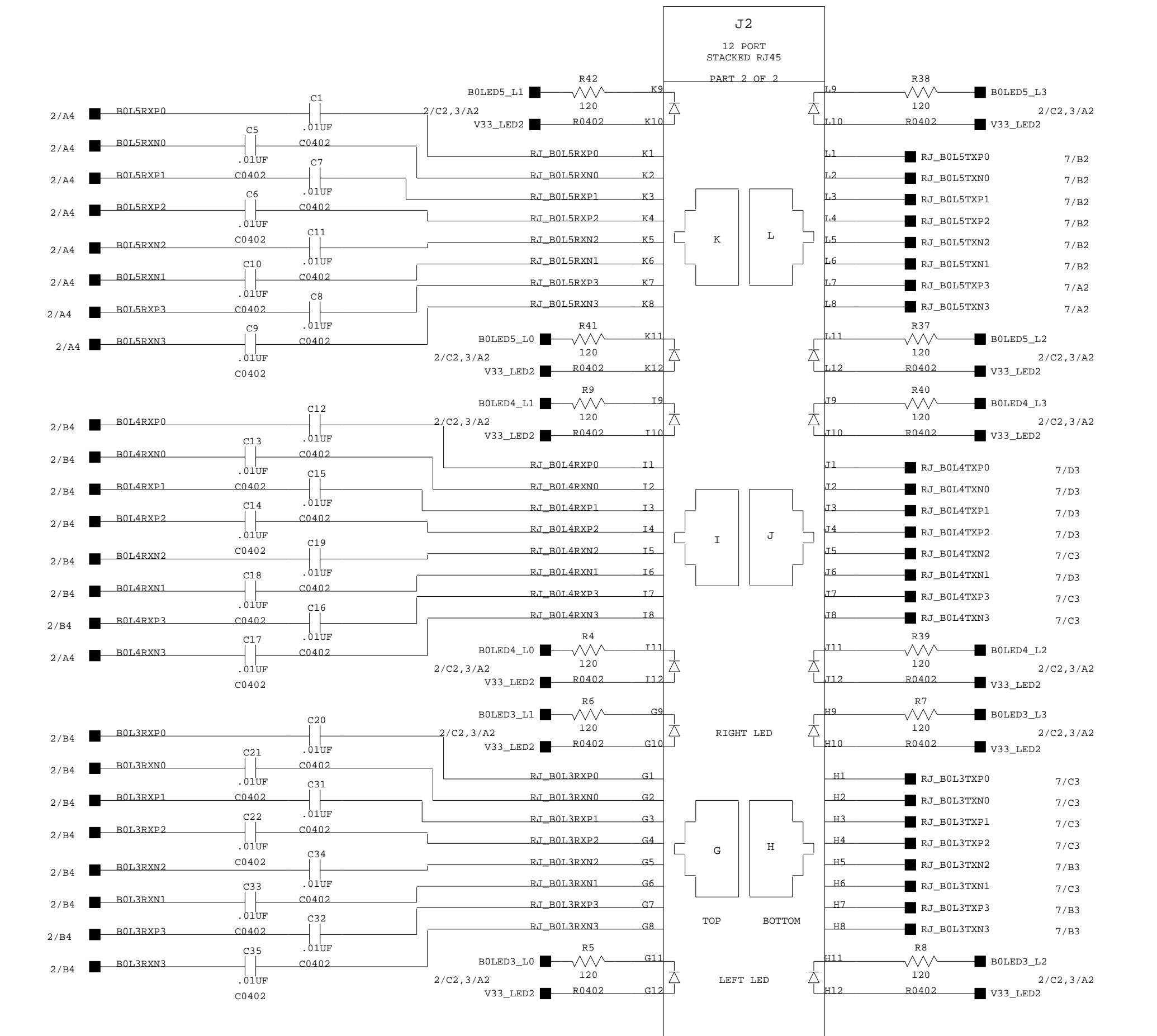
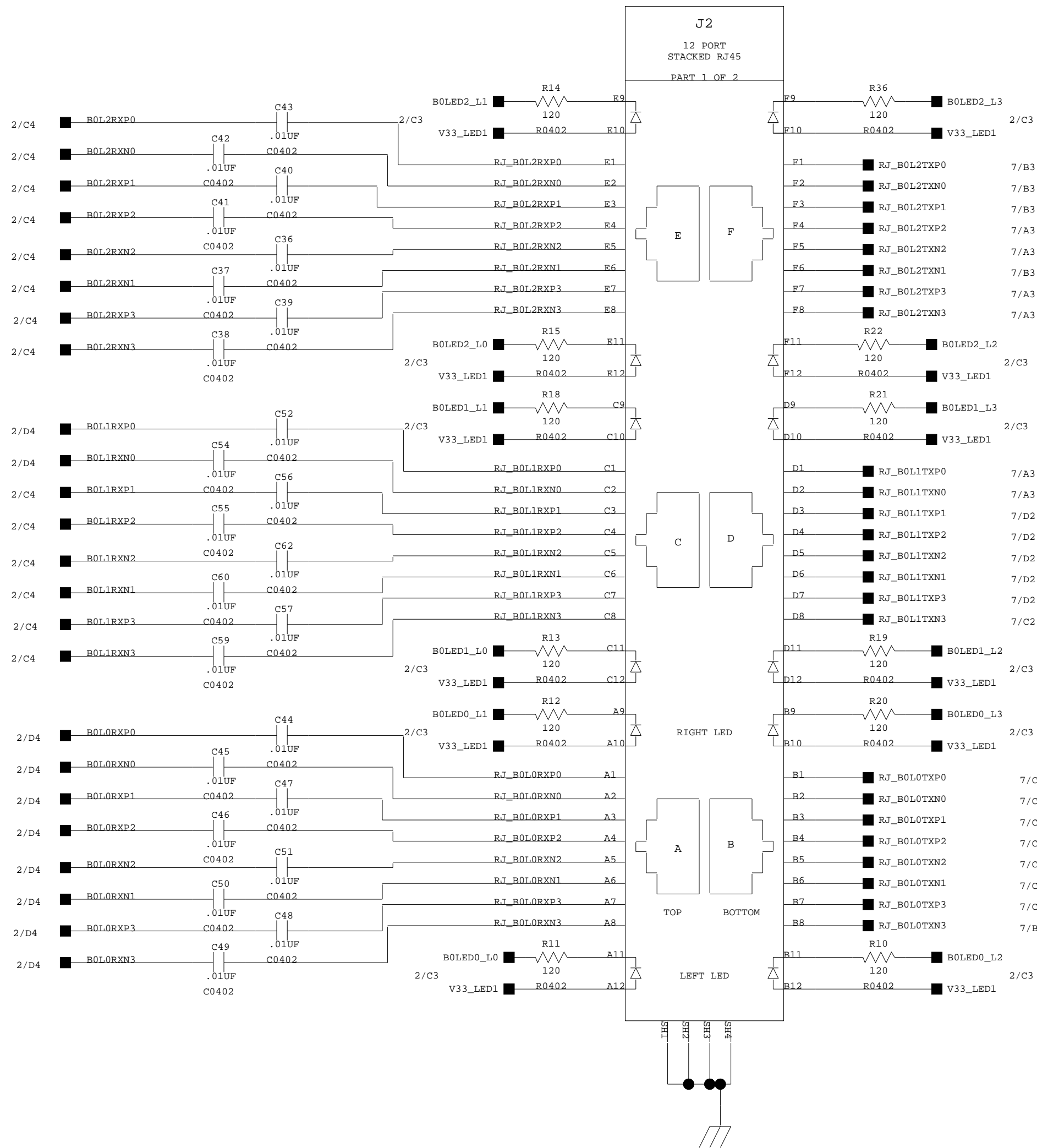


SG1010 U4 POWER & GND



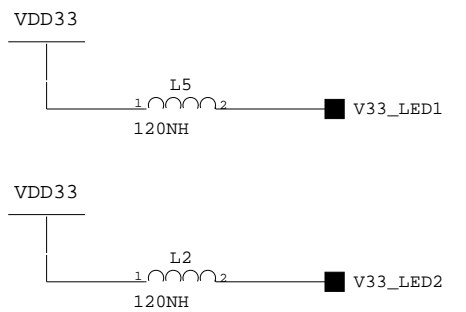
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SHEET 2 OF 7		DRAWING NUMBER	

SG1010 RJ45 CONNECTOR

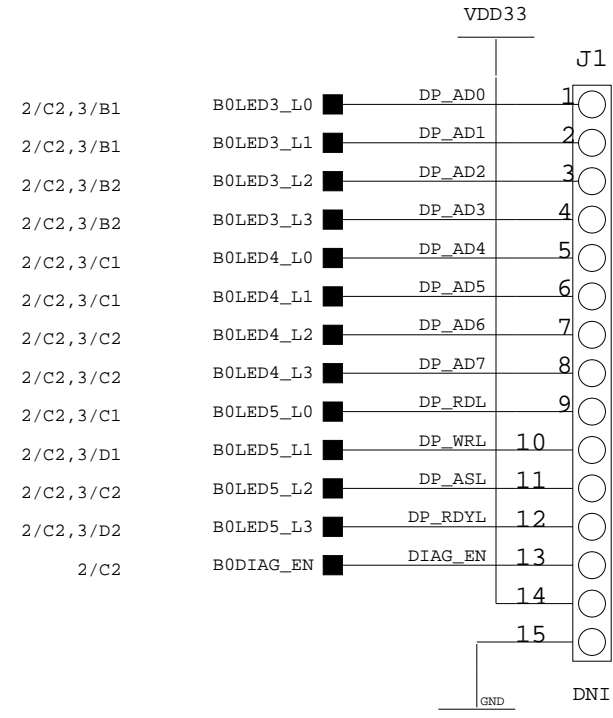


LINK LED INDUCTORS

Place Ind across plane split



SG1010 DIAG

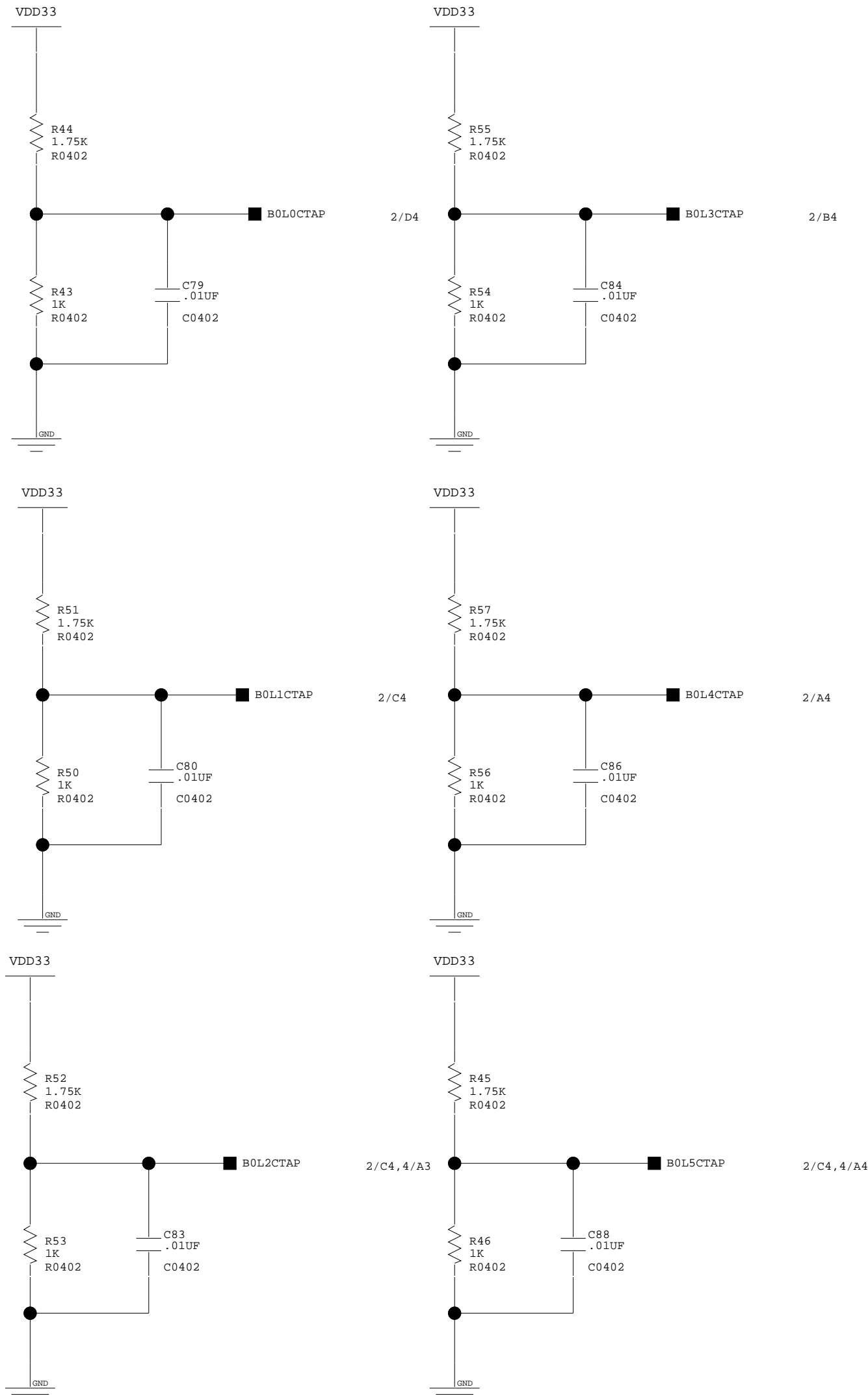


RJ45 CONNECTOR, LNK LEDS

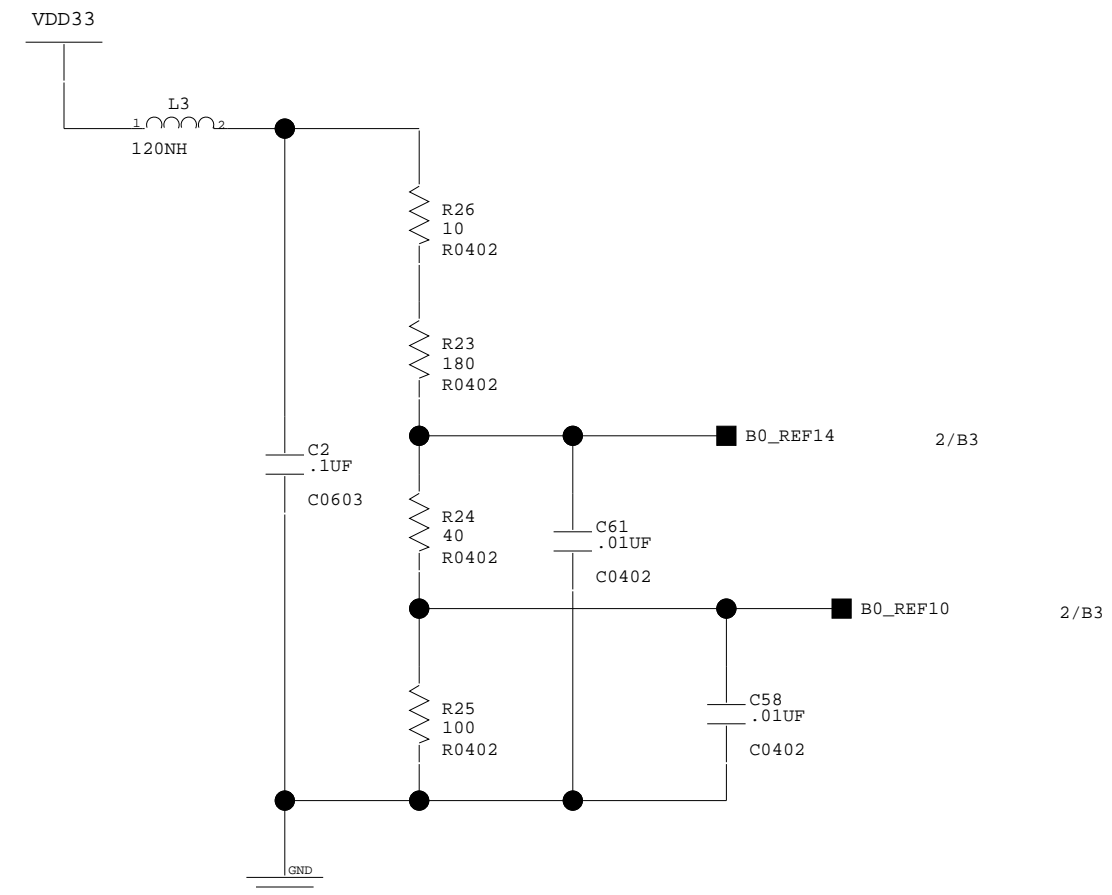
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Drawn	Keith P	Date	10-15-2003_15:52	SHEET 3 OF 7

NOTE: PLACE A 0.01UF CAPACITOR
 AS CLOSE TO THE ASSOCIATED
 INPUT PIN ON ALL SG1010'S AS POSSIBLE.
 NOTE: ALL RESISTORS ON THIS PAGE
 ARE TO BE 1% TOLERANCE.

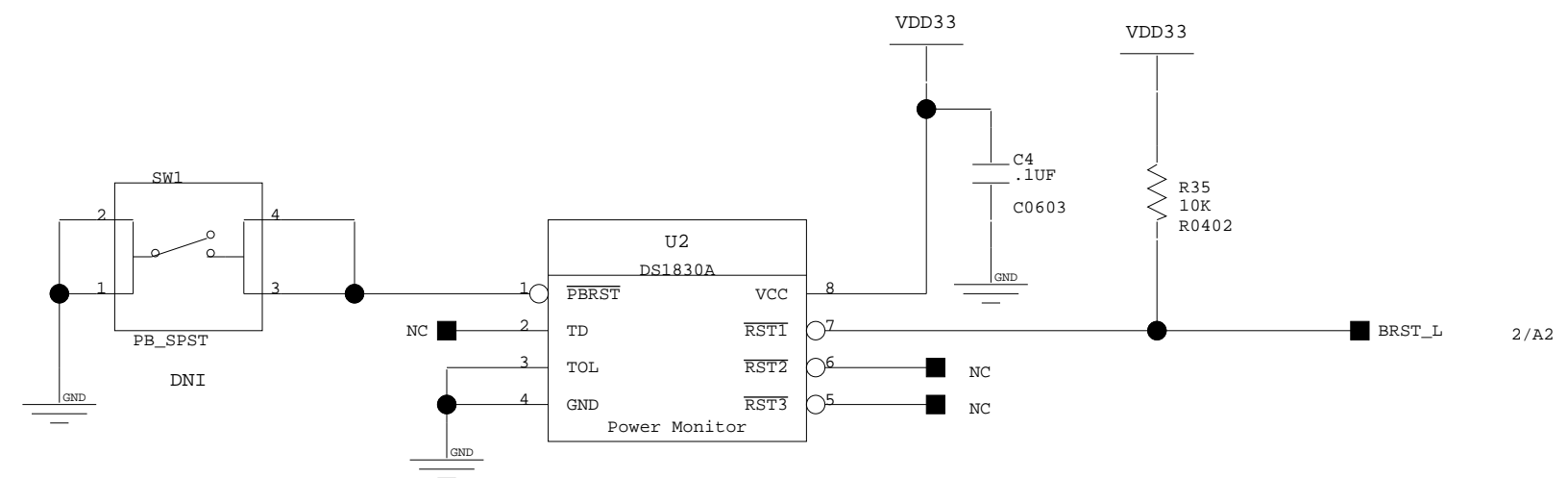
SG1010 CTAP VOLTAGES AC coupled



SG1010 REF14 & REF10



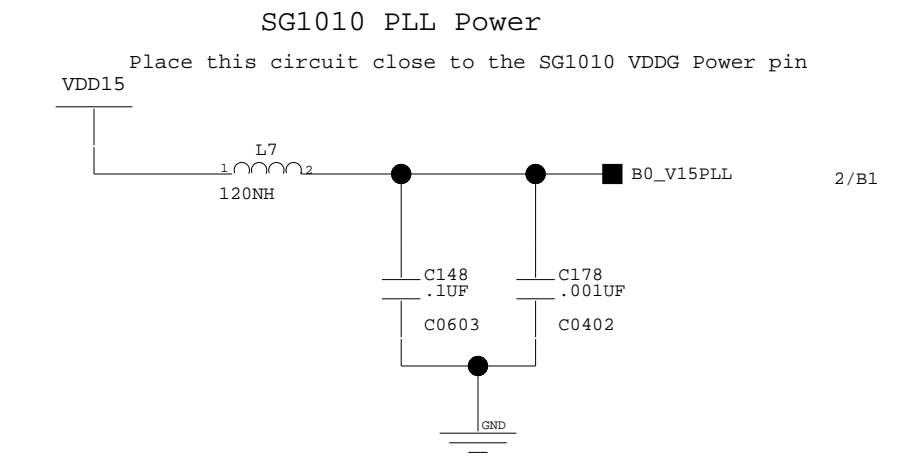
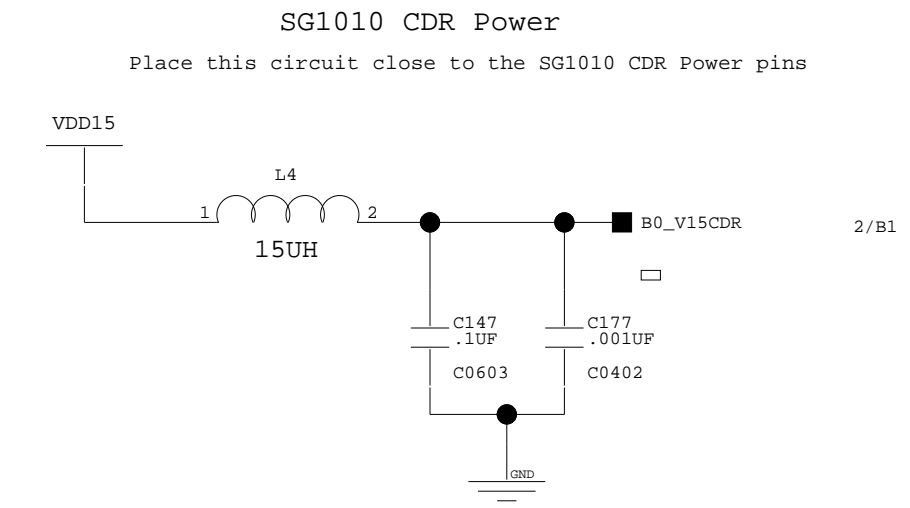
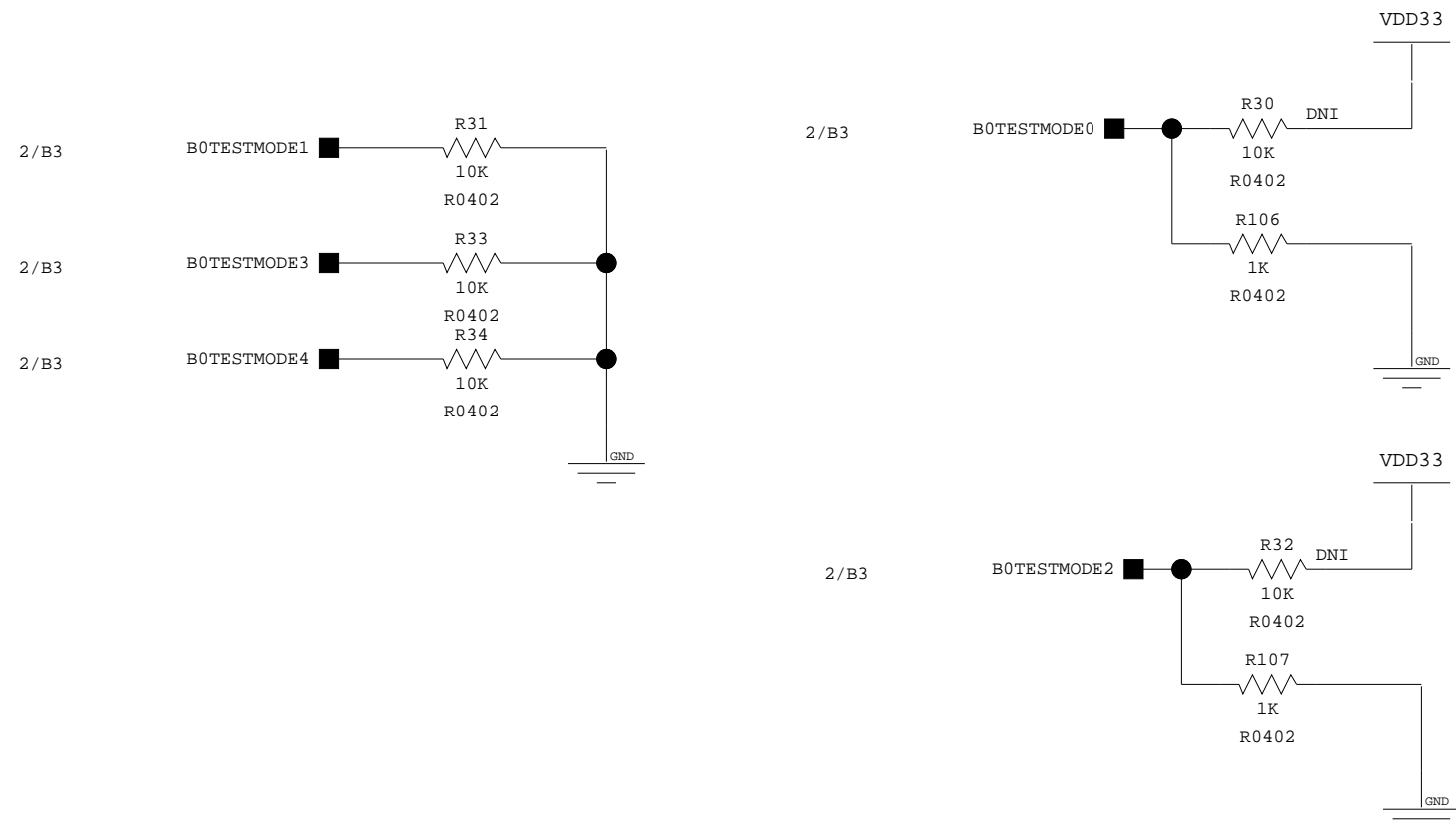
Power On Reset Circuit



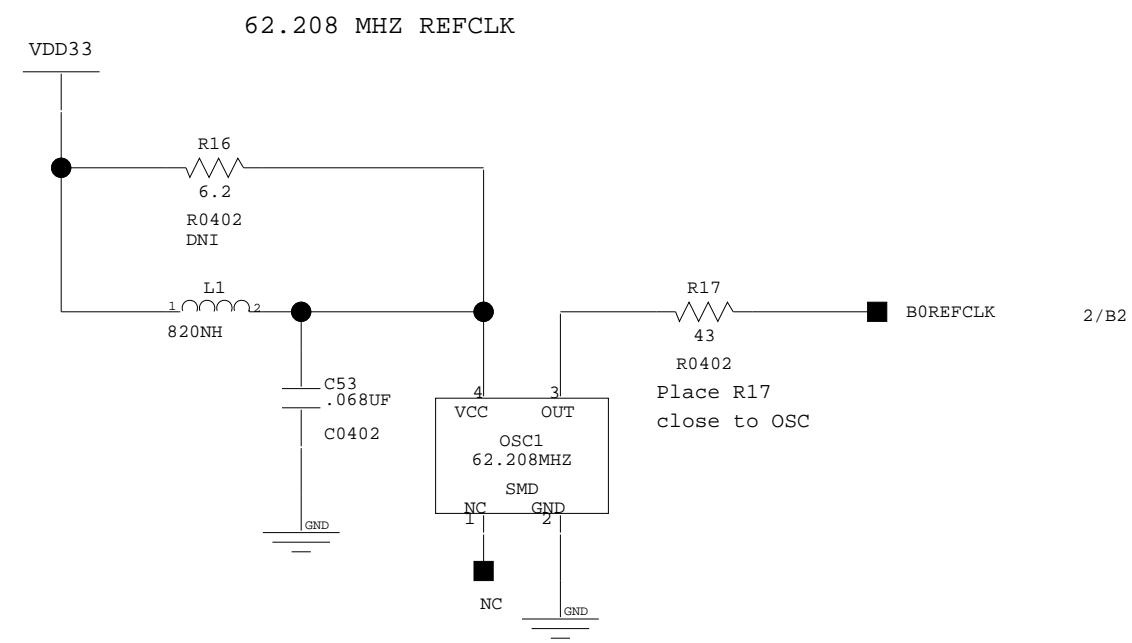
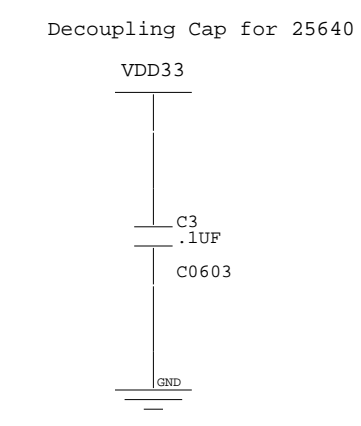
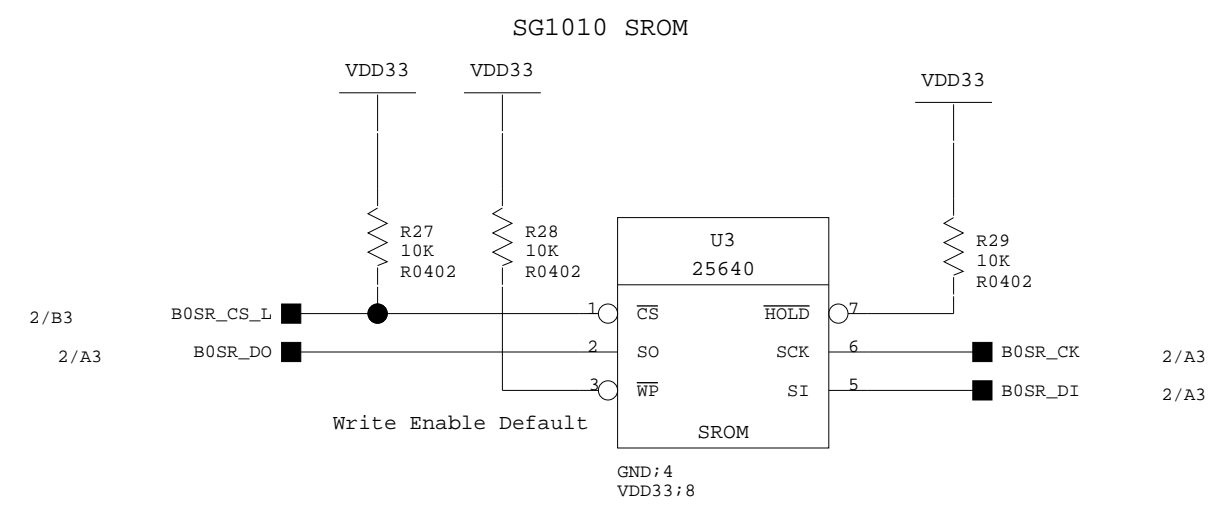
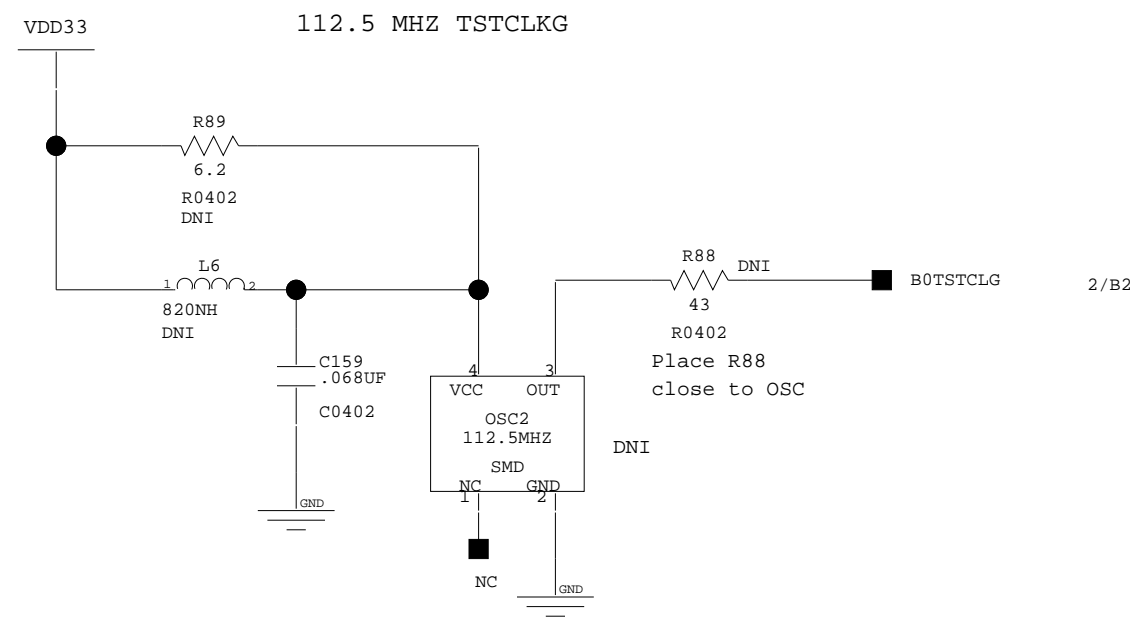
CTAP, REF10 AND REF14, PWR-ON RST

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SG1010 TESTMODE



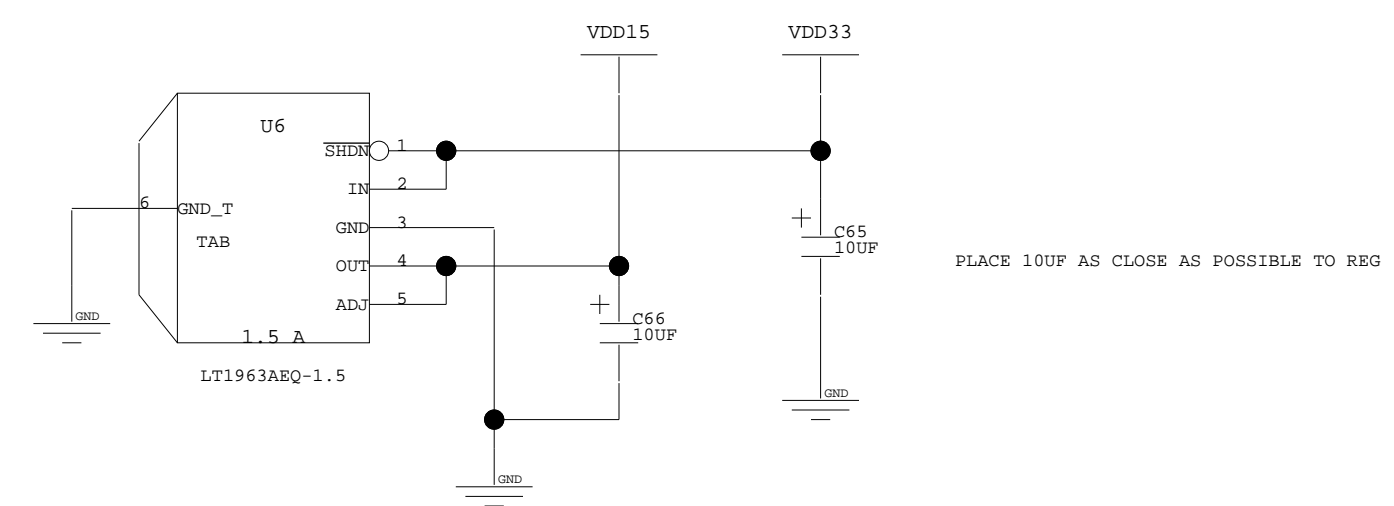
DO NOT INSTALL THIS CIRCUIT



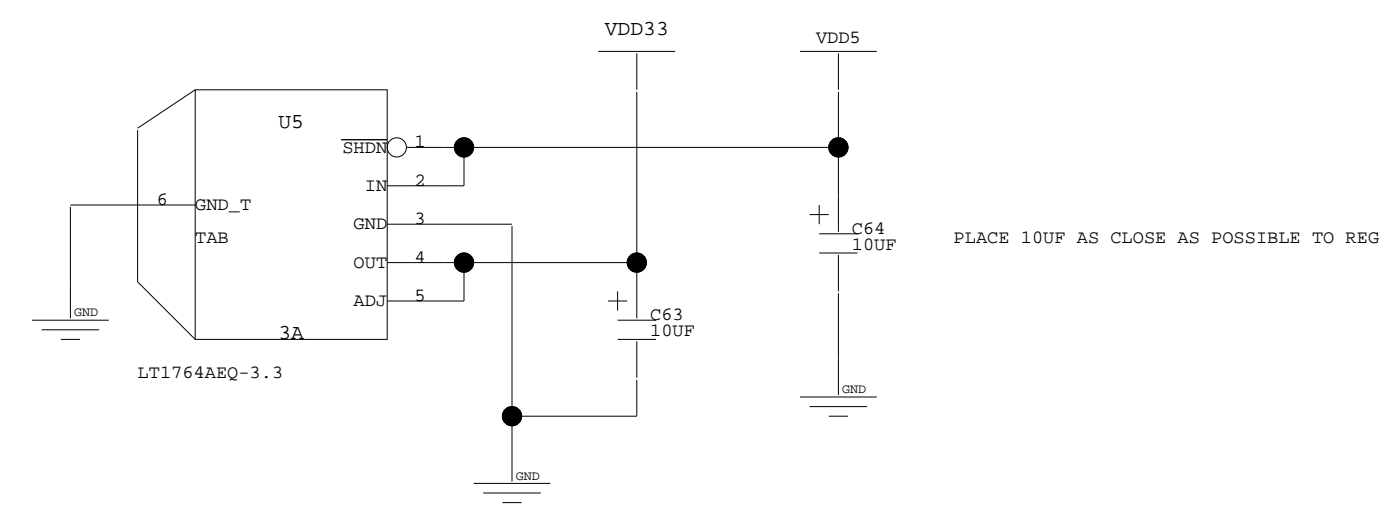
TSTMODE, SR0MS, ANLG PWR, CLKS

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SHEET		5 OF 7	

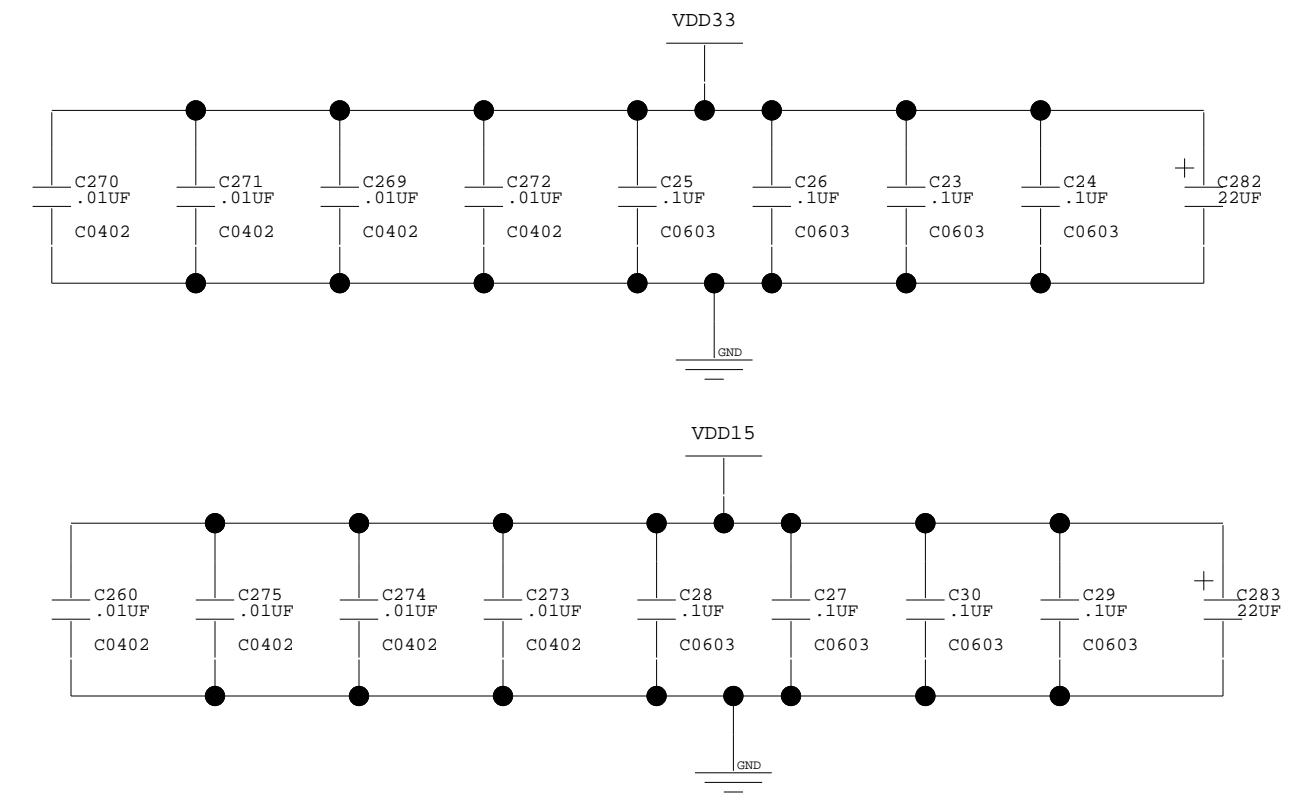
1.5 V REG



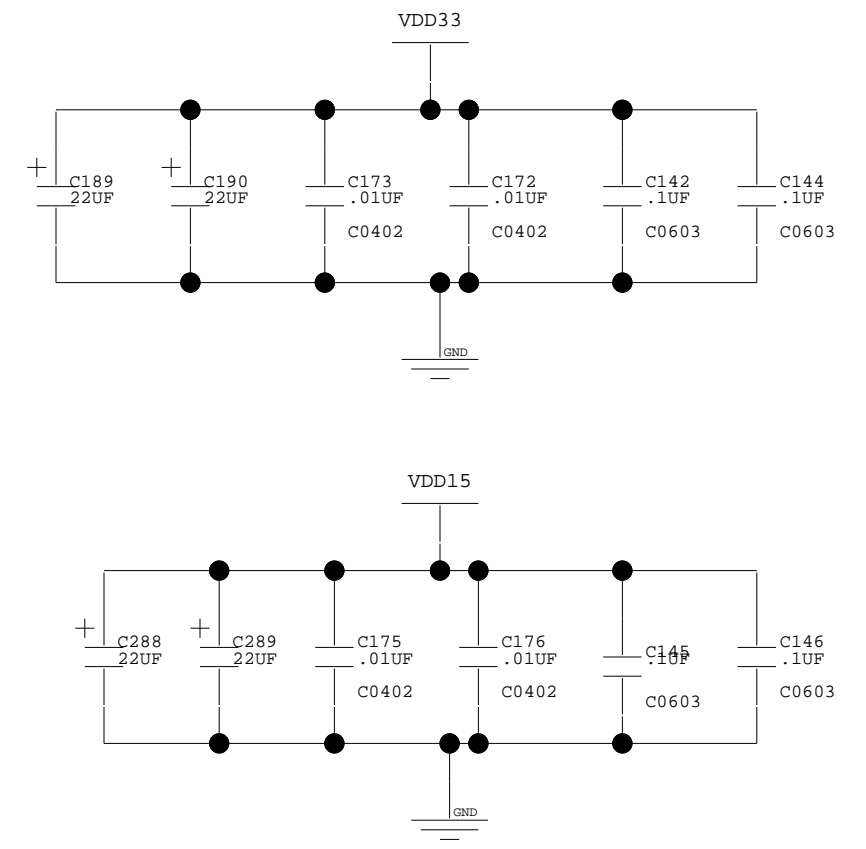
3.3 V REG



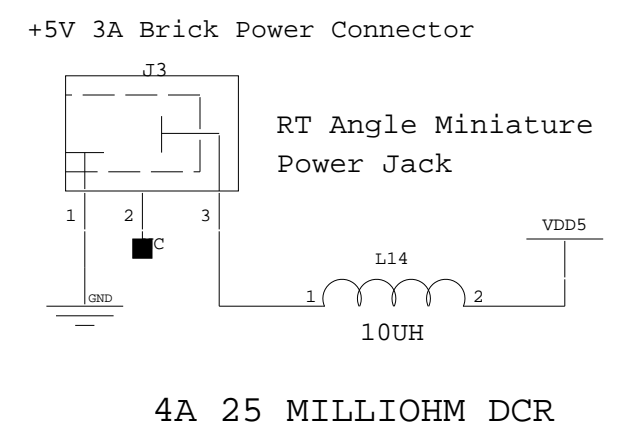
SG1010 DECOUPLING CAPACITORS



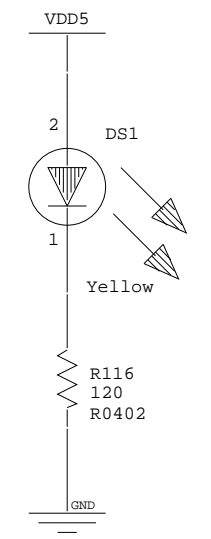
SPREAD THESE CAPACITORS THROUGHOUT BOARD



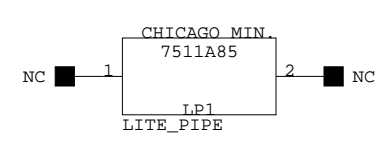
Power Connector



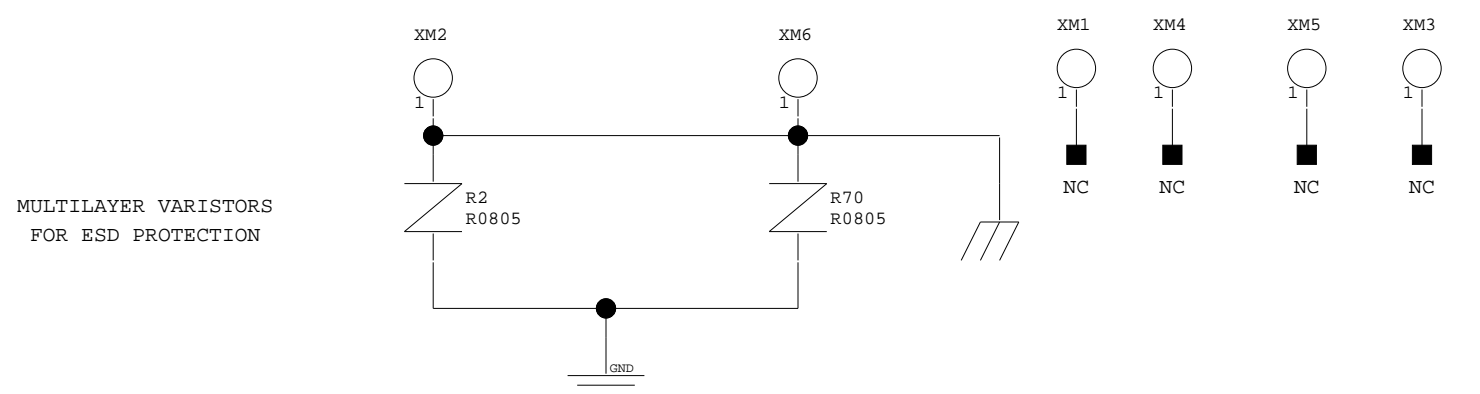
5V Power LED



LED Light PIPE



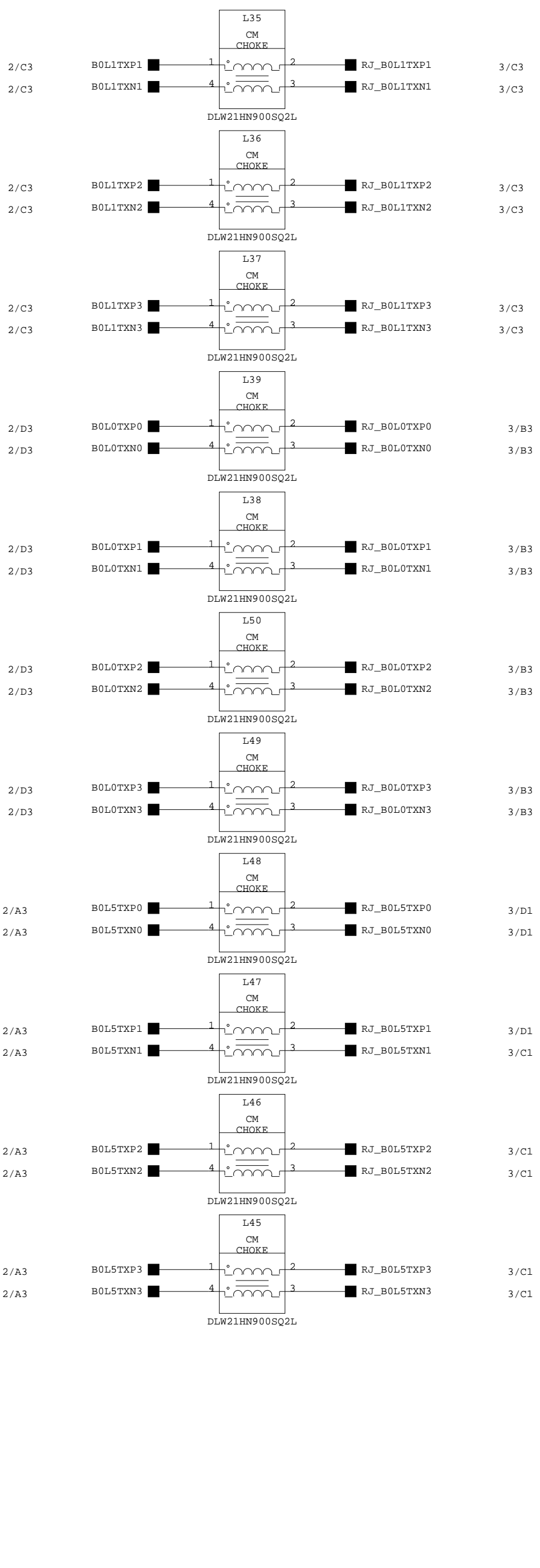
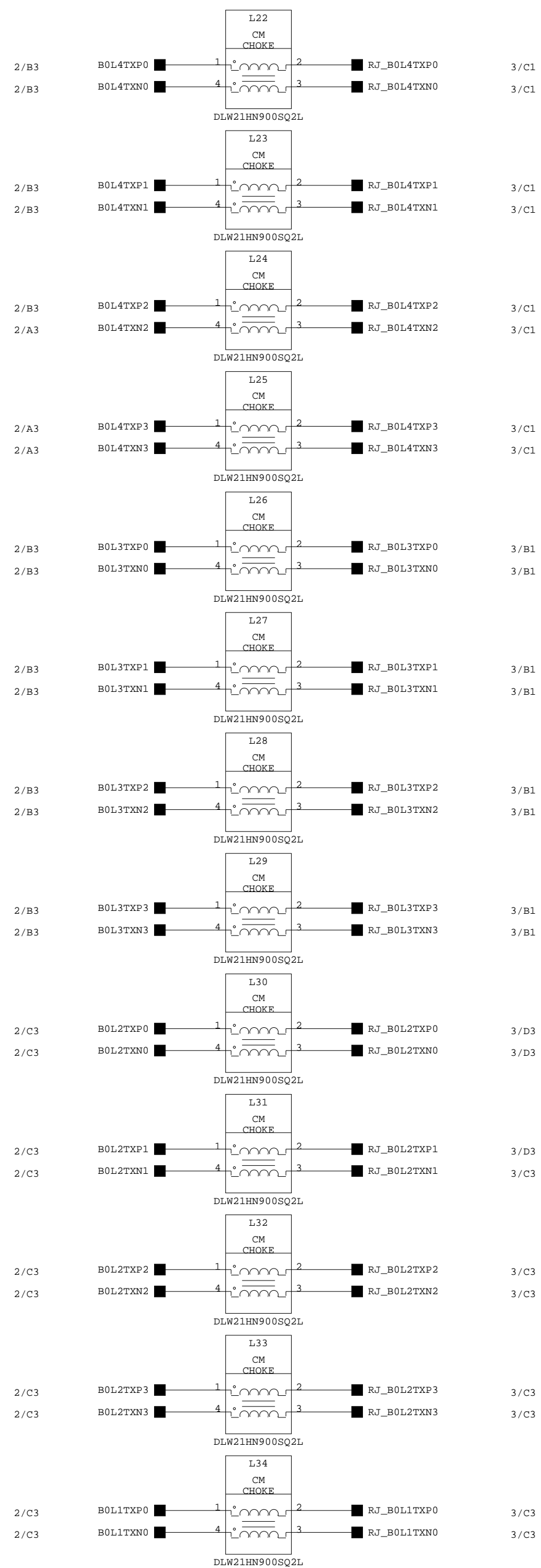
MOUNTING HOLES



1.5V REG & 3.3V REG, DECOUPLING CAPS

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SG1010 COMMON MODE CHOKES



COMMON MODE CHOKES

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Drawn Keith P	Date	10-15-2003_15:52		SHEET 7 OF 7